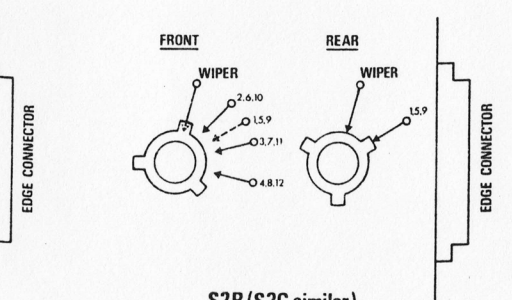
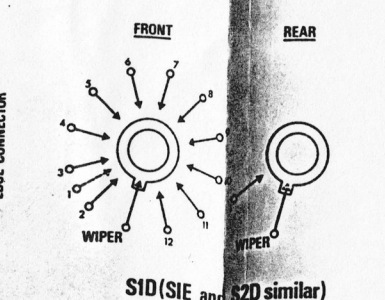
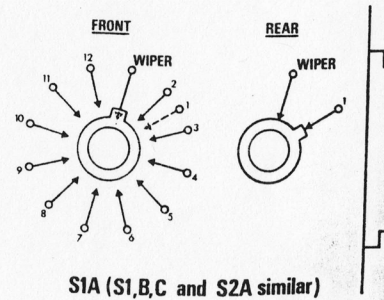
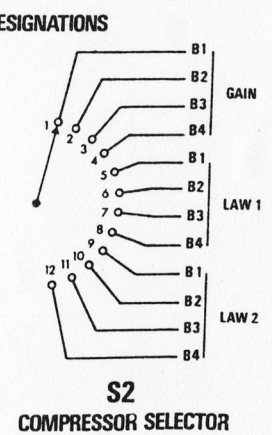
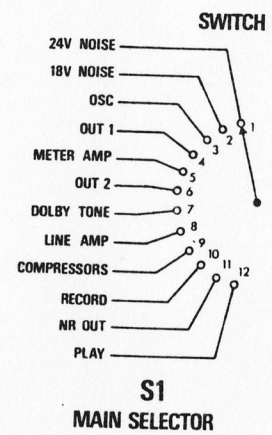


COMPONENT TOLERANCES

- ◆ ± 1%
- ◆◆ ± 2%
- ALL OTHER RESISTERS ± 5%
- ALL OTHER CAPACITORS ± 20%

- GENERAL NOTES**
1. SOCKET DESIGNATION OF CAT No.35A EDGE CONNECTOR (JF4), MATES WITH CAT No.22 NOISE REDUCTION MODULE, DESIGNATED (A).
 2. PC CONTACTS ON CAT No.35B (JM5), MATES WITH CHASSIS EDGE CONNECTOR IN CAT No.22 INSTALLATION.
 3. IC PIN NUMBERS IN BRACKETS REFER TO 14 PIN PACK.



- SWITCH NOTES**
1. ALL WAFERS ARE VIEWED FROM THE XMOB END.
 2. WAFERS ARE SHOWN RELATIVE TO EDGE CONNECTORS.
 3. WAFERS S1B & D HAVE NON-SHORTING BLADES. ALL OTHER WAFERS HAVE SHORTING BLADES.